07-03-06

Atty. Docket No. <u>JP920000046US1 (8728-728)</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):	Makoto Ueda et al.	Examiner: Kenneth Tang
SERIAL NO.:	09/682,188	Group Art Unit: 2195
FILED:	August 2, 2001	
FOR:	PROCESSOR MODULE FOR A MULTIPROCESSOR SYSTEM AND TASK ALLOCATION METHOD THEREOF	

Dated: June 30, 2006

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Sir:

Applicant submits herewith nine (9) sheets of formal drawings

depicting FIGS. 1-11 for this application.

Respectfully submitted,

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